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### Important information regarding the Programme Regulations

Last revised 30 June 2022

As a student registered with the University of London you are governed by the current General Regulations and Programme Regulations associated with your programme of study.

These Programme Regulations are designed and developed by the University of London which is responsible for the academic direction of the programme. The Programme Regulations will provide the detailed rules and guidance for your programme of





### **Passing assessments**

4 2

The pass mark for each module is 50%. Where there is more than one element of assessment for a module, you

#### Late submission of coursework elements

#### 4 12

You must keep to the deadlines given on the VLE. Coursework elements that are submitted after the deadline will not be marked and the attempt will be considered invalid.

See regulations 4.4 to 4.8 for more information on invalid attempts.

### 5 Number of attempts permitted at an assessment element

#### 5.1

The maximum number of attempts permitted for any element of assessment is two.

#### 5.2

You will fail the assessment if your overall weighted mark for the module is below 50%.

#### 5.3

You must make a second attempt at the assessment for a module you have failed, provided that you have not exceeded the maximum number of attempts at the assessment/s. If there are two elements of assessment for the module, all assessment elements will need to be attempted.

#### 5.4

lf you pass the module overall with a mark of 50% or above, you will not be permitted to make a BTO g/TT1 \$ቂጩኯ₫**ቬቸ**θ<mark>ዋ</mark>ዎቸ**ᠮ**ል **4ሰ**ሃወቋፍ**ቼይ**ዴቴክሎያ <del></del>ምንዶ(ቀ)72.995(ቀ)ዖ84ዓምሪቲ የወን-2.1998 (ውነ)ቭሁድ/የዕላቀጋ ፣ 5/95୮32184049፻ይት ቀህላ*ቻ/*104ሰ ወ15

As the entrance requirements for the PGDip and MSc are the same, you do not need

Mark range		Outcome
60%	69%	

# **Appendix A – Structure of the qualifications**

**MSc Computer Science** 

# **PGCert Computer Science**

For the qualification of PGCert Computer Science, you must pass

Any four modules (each worth 15 841.92 reW\*nBT/F2 11.04 Tf1 0 0 1 177.98 715.18 Tm0 g0 G[ )]TJET

large-scale applications and systems. Students learn

Memory management: allocation and protection, virtual memory with paging and segmentation.

Input-Output systems: bus architecture, interrupt

computer arithmetic, strings, graphs, sets, digital circuits, lists, binary trees; introduce

# **Appendix C – Assessment criteria**

### **Coursework and Project Assessment Criteria**

This is an indicative description of expectations at each grade level. Overall grades will comprise qualitative and quantitative